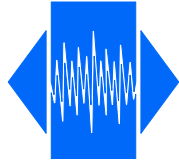


VT7-705-SQ-HP

High precision, reliable, temperature compensated
SMD VC-TCXO

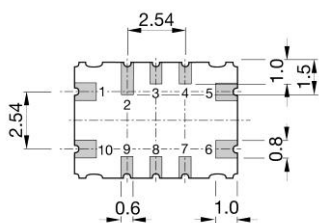
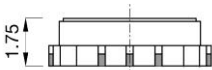
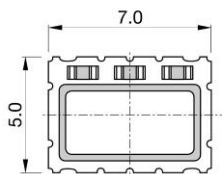
QuartzCom
the communications company



Features

- Applications: instrumentation, mobile radio, satellite navigation
- High frequency stability vs. temperature: $\pm 0.10 \sim \pm 0.5$ ppm
- Output signal Clipped sine wave or CMOS
- Low phase noise, high reliability

Parameter	Specification	
Frequency range	5 ~ 52 MHz	
Standard frequencies	5.0, 6.40, 9.60, 10.0, 12.80, 13.0, 16.0, 16.384, 18.432, 19.20, 19.44, 20.0, 25.0, 26.0, 30.72, 32.0, 40.0, 49.152 & 50.0 MHz	
Frequency stability:		
Frequency stability vs. temperature reference to $(F_{MAX}+F_{MIN})/2$	$\leq \pm 0.28$ ppm & $\leq \pm 0.50$ ppm	-40 ~ +85 °C standard
vs. aging	$\leq \pm 0.10 \sim \pm 0.25$ ppm	-40 ~ +85 °C on request
G-sensitivity	< 1.5 ppb/g	Gamma Γ standard
	< 0.3 ppb/g	Gamma Γ on request
Frequency tolerance ex. factory @ +25 °C	$\leq \pm 1.0$ ppm standard	$\leq \pm 0.5$ ppm on request
Supply voltage (nominal value ± 5 %)	+2.8 V or +5.0 V	standard 2.8 V, 3.0 V, 3.3 V & 5.0 V
Supply current	2 ~ 7 mA	
Output signal	clipped sine wave	CMOS
Output level	> 0.8 Vp-p	$V_{OH} > 0.9 \times V_{dc} / V_{OL} < 0.1 \times V_{dc}$
Output load	10 k Ω // 10 pF	15 pF Max.
Electronic Frequency Control (EFC) range	$\pm 5 \sim \pm 10$ ppm	
EFC voltage (Vc)	+1.5 V	± 1.0 V
Tri-state function	pin #9 \rightarrow high or open pin #9 \rightarrow low or GND	pin #6 \rightarrow oscillation pin #6 \rightarrow high impedance
Phase noise @ 40.0 MHz	-135 dBc/Hz	@ 1 kHz
Operating temperature range	-20 ~ +70 °C -40 ~ +85 °C -55 ~ +95 °C	commercial application industrial application advanced application
Storage temperature range	-55 ~ +105 °C	
Reflow Profiles as per IPC/JEDEC J-STD-020C	≤ 260 °C over 10 sec. Max.	
Moisture sensitivity	Level 1 (unlimited)	
Customer specifications on request		

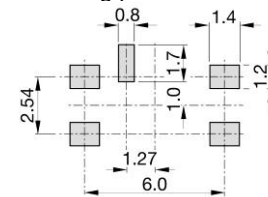


Pin function

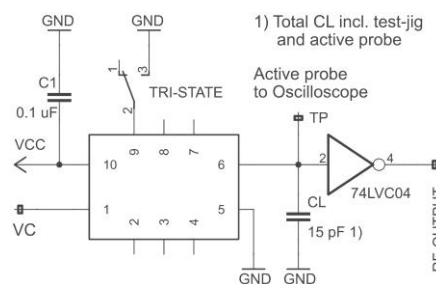
- # 1 Vc Voltage control
- # 5 GND
- # 6 Output
- # 9 Tri-state
- # 10 Vcc

Do not contact #2, #3, #4, #7 & #8

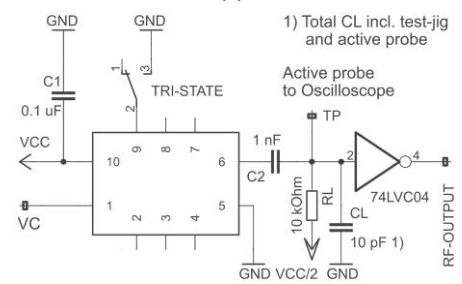
Soldering pattern



Test circuit for CMOS



Test circuit for Clipped Sine Wave



2011/65/EU RoHS compliant

24 Aug. 18

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