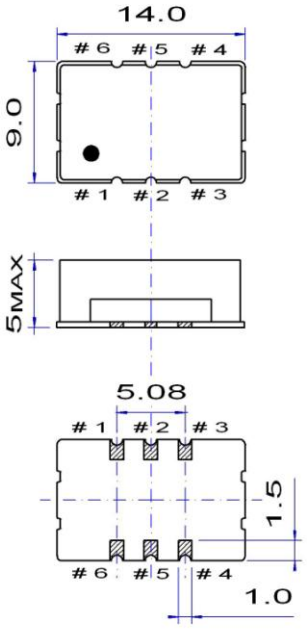
VXO-P9-DS-6p

SMD VCXO
LVDS

Features

- Applications: wireless communications, 10 Gigabit Ethernet, SDH, SONET, Fibre Channel, DSL, GPON
- Output frequency up to 312.5 MHz
- Low jitter < 0.5 ps

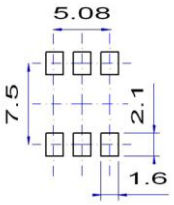
Parameter	Specification	
	VXO-P9-25DS-6p	VXO-P9-3DS-6p
Frequency range	19.44 ~ 312.5 MHz	
Standard frequencies	19.44, 76.80, 77.76, 106.25, 120.00, 122.88, 125.00, 153.60, 155.52 156.25, 159.375, 161.1328, 200.00, 212.5 & 256.00 MHz	
Supply voltage	+2.5 V ±5 %	+3.3 V ±5 %
Supply current	30 ~ 75 mA	
Frequency stability (all inclusive) (*)	< ±25 ppm over -20 ~ +70 °C < ±50 ppm over -40 ~ +85 °C	
Output signal	LVDS	
Output voltage	V_{OH} ≤ 1.60 V	V_{OL} ≥ 0.9 V
Output load	100 Ω	
Symmetry	45 ~ 55 %	@ ½ Vdc
Rise / Fall time	1 ns	
Frequency pulling range	±50 ~ ±100 ppm	
Voltage control	+1.25 V ±1.25 V	+1.65 V ±1.35 V
Enable / Disable function	Pin #2 = high or open Pin #4 & #5 ➔ (E) signal Pin #2 = low Pin #4 & #5 ➔ (D) no signal	
Jitter (rms)	< 0.5 ps	@ 12 kHz ~ 20 MHz from carrier frequer
Operating temperature range	-20 ~ +70 °C commercial application -40 ~ +85 °C industrial application	
Packaging units	tape & reel	500 or 1'000 pieces
(*) All inclusive	frequency stability vs. temperature, tolerance, aging, supply & load variation	
Customer specifications on request		



Pin function

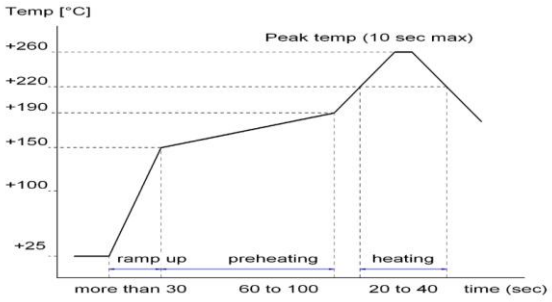
1 Vc Voltage control
2 E/D or not connected
3 GND
4 Output
5 Complementary output
6 Vdc

Example for solder pattern



Do not design any conductive path between the pattern

Example for IR reflow soldering temperature



2002/95/EC RoHS compliant

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